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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
08/520,079	08/28/1995	SHUNPEI YAMAZAKI		1321

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EXAMINER

BROCK II, PAUL E

ART UNIT PAPER NUMBER

2815

DATE MAILED: 10/15/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

08/520,079

Applicant(s)

YAMAZAKI ET AL.

Examiner

Paul E Brock II

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 August 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 73-94,96-116,123-141 and 143-155 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 73-94,96-116,123-141 and 143-155 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 28 August 1995 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date <u>8-18-04</u> . | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

Information Disclosure Statement

1. Reference US-6,616,613 cited on the information disclosure statement filed August 18, 2004 is a Patent to Goodman entitled "Physiological signal monitoring system" patented on September 9, 2003. This is believed to be an incorrect citation as the information disclosure statement credits Yamazaki et al. with this patent with a patent date of September 2, 2003. U.S. Patent 6,613,613 to Yamazaki on September 2, 2003 has been considered on the attached PTO-892.

Claim Rejections - 35 USC § 103

2. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

3. Claims 73 – 116, 123 – 141, and 143 – 155 are rejected under 35 U.S.C. 103(a) as being unpatentable over Zhang et al. (USPAT 5563426, Zhang¹).

With regard to claim 73, Zhang¹ discloses in figure 4c a thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island (3) over a substrate (1a) having an insulating surface (1b). Zhang¹ discloses in figures 4b and 4c source (25a and 25c) and drain regions (25b and 25d) in said semiconductor island. Zhang¹ discloses in

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figure 4b a channel forming region (between 25a and 25b in figure 4b) between said source and drain regions. Zhang¹ discloses in figures 4a – 4c a gate insulating film (22) adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode (23a) adjacent to said channel forming region having said gate insulating film therebetween, wherein said channel forming region has no grain boundary (4). No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a – 1c, 2a – 2d and 4a – 4c wherein said semiconductor island includes a spin density not higher than $1 \times 10^{17} \text{ cm}^{-3}$, because an identical spin density is a property that must be shared by products that result from two processes that are the same. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not greater than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not greater than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 74, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein the crystalline semiconductor island comprise a material of Ni.

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With regard to claim 75, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not greater than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not greater than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 76, Zhang¹ discloses in column 9, lines 38 – 45 a thin film transistor wherein said semiconductor island includes the point defect (oxygen) of less $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches wherein said semiconductor island includes a point defect of $1 \times 10^{16} \text{ cm}^{-3}$ or more. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to have said semiconductor island include a point defect of $1 \times 10^{16} \text{ cm}^{-3}$ or more in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ teaches in column 11, lines 47 – 56 that is obvious to have the hydrogen element for neutralizing the point defect at a concentration of 1×10^{18} .

With regard to claim 77, it is obvious in Zhang¹ wherein said semiconductor island includes the spin density not lower than $1 \times 10^{15} \text{ cm}^{-3}$.

With regard to claim 78, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 79, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times$

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10^{18} cm^{-3} , and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 80, Zhang¹ discloses in figure 4c a thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 4a – 4c a gate insulating film on at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode over said channel forming region having said gate insulating film therebetween, wherein said channel forming region has no grain boundary. Zhang¹ discloses in column 9, lines 38 – 45 a thin film transistor wherein said semiconductor island includes the point defect (oxygen) of less $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches wherein said semiconductor island includes a point defect of $1 \times 10^{16} \text{ cm}^{-3}$ or more. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to have said semiconductor island include a point defect of $1 \times 10^{16} \text{ cm}^{-3}$ or more in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes

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hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 81, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein the crystalline semiconductor island comprise a material of Ni.

With regard to claim 82, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 83, Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said semiconductor island includes hydrogen for neutralizing the point defect at a concentration less than $1 \times 10^{20} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the hydrogen concentration is not lower than $1 \times 10^{15} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are

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obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not lower than $1 \times 10^{15} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 84, No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a – 1c, 2a – 2d and 4a – 4c wherein said semiconductor island includes a spin density of 1×10^{15} to $1 \times 10^{17} \text{ cm}^{-3}$, because an identical spin density is a property that must be shared by products that result from two processes that are the same.

With regard to claim 85, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 86, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 87, Zhang¹ discloses in figure 4c a semiconductor device. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island on an

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insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of $20 - 100 \text{ cm}^2/\text{Vs}$. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of $200-400 \text{ cm}^2/\text{Vs}$. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of $200-400 \text{ cm}^2/\text{Vs}$ in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that

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of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 88, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 89, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 90, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 91, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$

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³, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 92, Zhang¹ teaches in figures 1a, 1b, 2a – 2d, and column 12, lines 1 – 30 wherein said monodomain region has a grain size of 50 μm or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50 – 100 μm when the metal portions are set from 25 – 50 μm apart as disclosed by Zhang¹ in column 12, lines 1 – 30.

With regard to claim 93, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said channel forming region is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹

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overlap. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 94, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 95, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

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With regard to claim 96, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 97, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 98, Zhang¹ teaches in figures 1a, 1b, 2a – 2d; and column 12, lines 1 – 30 wherein said monodomain region has a grain size of 50 μm or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50 – 100 μm when the metal portions are set from 25 – 50 μm apart as disclosed by Zhang¹ in column 12, lines 1 – 30.

With regard to claim 99, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in column 9, lines 38 – 45 a p-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 an n-channel thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a –

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1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region.

Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary.

Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 100, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 101, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in

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the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 102, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 103, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 104, Zhang¹ teaches in figures 1a, 1b, 2a – 2d; and column 12, lines 1 – 30 wherein said monodomain region has a grain size of 50 μm or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50 – 100 μm when the metal portions are set from 25 – 50 μm apart as disclosed by Zhang¹ in column 12, lines 1 – 30.

With regard to claim 105, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in column 9, lines 38 – 45 a p-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 an n-channel thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures

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4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said channel forming region is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

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With regard to claim 106, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 107, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 108, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 109, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

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With regard to claim 110, Zhang¹ teaches in figures 1a, 1b, 2a – 2d, and column 12, lines 1 – 30 wherein said monodomain region has a grain size of 50 μm or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50 – 100 μm when the metal portions are set from 25 – 50 μm apart as disclosed by Zhang¹ in column 12, lines 1 – 30.

With regard to claim 111, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in figures 8a and 8b, and column 9, lines 28 - 37 an active matrix circuit portion including at least a first thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 a driving circuit portion including at least a second thin film transistor. Zhang¹ discloses in figures 1a – 1c, 2a - 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of

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Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 112, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 113, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 114, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 115, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of

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ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 116, Zhang¹ teaches in figures 1a, 1b, 2a – 2d, and column 12, lines 1 – 30 wherein said monodomain region has a grain size of 50 μm or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50 – 100 μm when the metal portions are set from 25 – 50 μm apart as disclosed by Zhang¹ in column 12, lines 1 – 30.

With regard to claim 123, Zhang¹ discloses in figure 4c a semiconductor device. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said source and drain regions. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island is formed in a monodomain region which contains no grain boundary. Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$ in the device of

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Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a – 1c, 2a – 2d and 4a – 4c wherein said semiconductor device has a S value of 0.03-0.3, because an identical S value is a property that must be shared by products that result from two processes that are the same. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes at least one selected from the group consisting of a p-channel thin film transistor and an n-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 – 100 cm^2/Vs . Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm^2/Vs . Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm^2/Vs in the device of Zhang¹. This is because the prior art teaches a suitable

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method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes an n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 124, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 125, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in

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the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 126, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 127, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 128, Zhang¹ teaches in figures 1a, 1b, 2a – 2d, and column 12, lines 1 – 30 wherein said monodomain region has a grain size of 50 μm or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50 – 100 μm when the metal portions are set from 25 – 50 μm apart as disclosed by Zhang¹ in column 12, lines 1 – 30.

With regard to claim 129, Zhang¹ discloses in figure 4c semiconductor device. Zhang¹ discloses in figures 1a – 1c, 2a – 2d, and 4a – 4c a crystalline semiconductor island on an insulating surface. Zhang¹ discloses in figures 4b and 4c source and drain regions in said semiconductor island. Zhang¹ discloses in figure 4b a channel forming region between said

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source and drain regions. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate insulating film adjacent to at least said channel forming region. Zhang¹ discloses in figures 1a- 1c, 2a – 2d, and 4a – 4c a gate electrode adjacent to said channel forming region having said gate insulating film therebetween, wherein said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, wherein said channel forming region is formed in a monodomain region which contains no grain boundary. No differences have been pointed out in the formation of the channel forming region of Zhang¹ and the channel forming region of the current pending claim in view of the currently pending specification. Therefore Zhang¹ must teach in figures 1a – 1c, 2a – 2d and 4a – 4c wherein said semiconductor device has a S value of 0.03-0.3, because an identical S value is a property that must be shared by products that result from two processes that are the same. Zhang¹ discloses in figures 4c and column 11, lines 47 – 56 wherein said crystalline semiconductor island includes hydrogen at concentration less than $1 \times 10^{20} \text{ cm}^{-3}$ (i.e. the known atomic density of Si is 10^{22} cm^{-3} , less than 5% of 10^{22} is less than 10^{20}). It is not clear if Zhang¹ teaches that the hydrogen concentration is not higher than $1 \times 10^{20} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the hydrogen atom concentration of not higher than $1 \times 10^{20} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes at least one selected from the group consisting of a p-channel thin film transistor and an n-channel thin film transistor. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 – $100 \text{ cm}^2/\text{Vs}$. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel

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thin film transistor having mobility in a range of 200-400 cm^2/Vs . Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm^2/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes an n-channel thin film transistor having a mobility in a range of 30 – 150 cm^2/Vs . Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm^2/Vs . Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm^2/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention. Further, any changes in particular device concentrations or properties would have been routine experimentation for one of ordinary skill seeking to maximize device function in the device of Zhang¹.

With regard to claim 130, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 wherein said crystalline semiconductor island comprises a material Ni.

With regard to claim 131, Zhang¹ discloses in figures 1a, 1b, and 2a; and column 6, lines 19 – 40 a thin film transistor wherein said material is included in said semiconductor island at a concentration less than $5 \times 10^{19} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that the material is included

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in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use the material included in the semiconductor at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 132, Zhang¹ discloses in column 4, lines 18 – 20 wherein said semiconductor island is a silicon island.

With regard to claim 133, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not lower than $1 \times 10^{16} \text{ cm}^{-3}$, and oxygen at a concentration not lower than $1 \times 10^{17} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 134, Zhang¹ teaches in figures 1a, 1b, 2a – 2d; and column 12, lines 1 – 30 wherein said monodomain region has a grain size of 50 μm or more. It should be noted that the crystal grains (3) grown around metal portions (2) must have a grain size of 50 – 100 μm when the metal portions are set from 25 – 50 μm apart as disclosed by Zhang¹ in column 12, lines 1 – 30.

With regard to claim 135, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 136, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 137, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy

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(SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 138, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 139, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to

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understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 140, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 141, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill¹ in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 143, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not

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define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 144, Zhang¹ discloses in column 9, lines 38 – 45 wherein each of the concentrations of carbon, nitrogen and oxygen is measured. A method of measuring does not define a patentable feature in a device claim. Therefore, measuring the concentration of carbon, nitrogen and oxygen by the well known technique of secondary ion mass spectroscopy (SIMS) does not bear any patentable weight in this device claim. Further, it would have been obvious to one of ordinary skill in the art at the time of the present invention to use the technique of SIMS to measure the concentration of carbon, nitrogen and oxygen in the device of Zhang¹ in order to understand the electrical characteristics of the device as it relates to these features. This understanding would result in better device control.

With regard to claim 145, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of

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ordinary skill in the art to use carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 146, Zhang¹ discloses in column 9, lines 38 – 45 wherein the thin film transistor is an n-channel thin film transistor having a mobility in a range of 30 – 150 cm^2/Vs . Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm^2/Vs . Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm^2/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 147, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

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With regard to claim 148, Zhang¹ discloses in column 9, lines 38 – 45 wherein the thin film transistor is one of a p-channel thin film transistor having a mobility in a range of 20 – 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 149, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 150, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not

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higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 151, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$, and oxygen at a concentration not higher than $5 \times 10^{19} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 152, Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 – 100 cm^2/Vs . Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm^2/Vs . Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm^2/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact

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that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility in a range of 500-1000 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 153, Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a p-channel thin film transistor having a mobility in a range of 20 – 100 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm²/Vs. Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm²/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

Zhang¹ discloses in column 9, lines 38 – 45 wherein the semiconductor device includes a n-channel thin film transistor having a mobility in a range of 30 – 150 cm²/Vs. Zhang¹ is silent to the fact that the semiconductor device includes an n-channel thin film transistor having mobility

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in a range of 500-1000 cm^2/Vs . Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free n-channel monodomain region with mobility in a range of 500-1000 cm^2/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

With regard to claim 154, Zhang¹ discloses in column 9, lines 38 – 43 wherein the crystalline semiconductor island includes carbon and nitrogen at a concentration less than $1 \times 10^{18} \text{ cm}^{-3}$. It is not clear if Zhang¹ teaches that said crystalline semiconductor island includes carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$. MPEP 2144.05 states that overlapping ranges are obvious. It would have been obvious to one of ordinary skill in the art to use carbon and nitrogen at a concentration not higher than $5 \times 10^{18} \text{ cm}^{-3}$ in the device of Zhang¹ because the current claimed range and the disclosed range in Zhang¹ overlap.

With regard to claim 155, Zhang¹ discloses in column 9, lines 38 – 45 wherein the second thin film transistor is one of a p-channel thin film transistor having a mobility in a range of 20 – 100 cm^2/Vs . Zhang¹ is silent to the fact that the semiconductor device includes a p-channel thin film transistor having mobility in a range of 200-400 cm^2/Vs . Mobility is a function of the purity of the single crystal (monodomain) semiconductor. MPEP section 2144.04, VII teaches that it is obvious to one of ordinary skill in the art to have a more purely defect free p-channel monodomain region with mobility in a range of 200-400 cm^2/Vs in the device of Zhang¹. This is because the prior art teaches a suitable method for obtaining the claimed mobility, and

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that fact that the monodomain region of Zhang¹ has the same utility as that of the claimed invention.

Response to Arguments

4. Applicant's arguments filed August 18, 2004 have been fully considered but they are not persuasive.

5. With regard to applicant's argument that Zhang¹ "does not disclose or suggest a channel-forming region having no grain boundary," it should be noted that Zhang¹ does in fact teach forming a channel region in a portion of a film with no grain boundaries. Zhang¹ teaches in figures 1a, 1b, and 2a – 2d forming a channel region (middle portion of 3) in a portion of a film with no grain boundaries (4). It should be noted that Zhang¹ that the single crystal semiconductor regions (6) are formed only from those portions of the recrystallized semiconductor that does not include grain boundaries (4). The channel-forming regions in figure 1c are the portions of 6 overlapped by gates 7, and thus clearly have no grain boundaries. Therefore, applicant's arguments are not persuasive, and the rejection is proper.

6. With regard to applicants argument that "Zhang '426 include grain boundaries, since crystal grains grow radially from island nickel region 2 and grain boundaries are generated along with the crystal growth. This technical issue is taught and supported by, e.g., Fig. 2A and column 4, lines 38-57 of U.S. Patent No. 6,011,275 issued to Ohtani et al., and Fig. 5A, 5B, and column

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8, lines 27-51, for example of U.S. Patent No. 5,894,137 issued to Yamazaki et al.” This reasoning is not understood. Ohtani ‘275 teaches crystal growth using a “metal element added region 105” in figure 2a and column 4, lines 38 – 57. Yamazaki ‘275 teaches crystal growth incorporating designated region 501 “where nickel has been introduced” in figures 5a, 5b and column 8, lines 27 – 51. Neither the present invention nor Zhang¹ have a similar “metal element added region” or “a region where nickel has been introduced”. The present invention and Zhang¹ both incorporate a crystal growth method using a metal deposited on an amorphous silicon layer. Processes using “the metal element added region” and “a region where nickel has been introduced” are different than the deposited metal used in the present invention and Zhang¹, and thus will not follow the crystal growth patterns of applicant’s argument. Therefore, applicant’s arguments are not persuasive, and the rejection is proper.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Paul E Brock II whose telephone number is (571) 272-1723. The examiner can normally be reached on 8:30 AM - 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner’s supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Paul E Brock II

A handwritten signature in black ink, appearing to read "Paul E Brock II", with a stylized, cursive script.